

Intel Hardware Accelerator Research Program

The Intel Hardware Accelerator Research Program platform within FAbRIC is a cluster of several standard x86 servers and ten Intel Broadwell with Integrated FPGA nodes. Each node contains a processor package with both an Intel Broadwell Xeon CPU and an Altera Arria 10 GX FPGA.

- Michael Adler (Michael.Adler@intel.com)
- David B. Sheffield (David.B.Sheffield@intel.com)
- Enno Luebbbers (Enno.Luebbbers@intel.com)
- Andrey Ayupov (Andrey.Ayupov@intel.com)
- Elizabeth Barnes (Elizabeth.Barnes@intel.com)